



Job Specification: Member or Senior Member Technical Staff

THE COMPANY

Doulos is a leading global independent brand name for training in the field of digital hardware design and verification. Doulos offers training and consulting services within the fields of FPGA and ASIC design and verification, EDA language standards including VHDL, SystemVerilog, and SystemC, FPGA technology, ARM processor technology, and embedded software. Doulos is headquartered in the UK with offices in Germany and the USA.

Further information about Doulos is available at www.doulos.com

LOCATION

Germany

GENERAL DESCRIPTION

The job involves teaching face-to-face training courses at customers' premises and in central locations, and on occasion providing consultancy in FPGA and ASIC design, verification, and related technical areas. The main characteristic of the job is the variety and stimulation it offers as you divide your time between customer-facing training and consultancy, back room development activities, and technical support to sales and marketing. The job also offers a rare opportunity to become personally recognized across the industry as a subject matter expert.

The job requires excellent technical skills in the area of digital or system-level design using FPGAs or ASICs (see skills list below). This must be coupled with excellent communication skills and a willingness to engage in a role that combines customer-facing work with background research and development activities.

The job will be office or home based, working as part of a virtual team. The job requires regular travel to customer sites throughout Germany, Europe and occasionally world-wide. Fluency in spoken and written German and English is required.

SPECIFIC JOB VACANCIES

Member or Senior Member, Technical Staff in the field of FPGA or ASIC design and verification, with experience in VHDL.

REQUIRED QUALIFICATIONS AND SKILLS

- Essential qualifications and skills
 - Degree qualified in Electronics/Physics/ComputerScience/Engineering/Maths or similar
 - Strong digital hardware design and verification skills using VHDL or Verilog
 - Excellent written and verbal communication skills
 - A desire to lead high quality technical training courses.
- Preferred experience
 - Experience using simulation and synthesis tools from Cadence, Mentor, or Synopsys
 - Familiarity with Xilinx, Altera, MicroSemi, or Lattice technologies
 - Experience with one or more of the following languages: SystemVerilog, e, Vera, C, C++, SystemC, Tcl, Perl, Unix scripting
 - Experience in a customer-facing role such as training, consultancy, or application engineering.



DUTIES

- Teach face-to-face, hands-on training courses at customers' premises and in central locations
- Provide consultancy to customers in FPGA and ASIC design and verification and related areas
- Travel to customer sites in Germany, Europe, and worldwide
- Develop and maintain Doulos training course materials
- Maintain an up-to-date knowledge of hardware design/verification languages, tools, and methods
- Provide support to marketing and sales activities
- Provide I.T. support related to the delivery of training courses
- Work as part of a virtual team dispersed around the world.